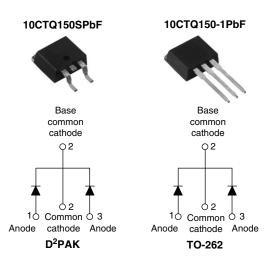




Vishay High Power Products

Schottky Rectifier, 2 x 5 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 5 A				
V _R	150 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	10	A		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	620	А		
V _F	5 Apk, T _J = 125 °C (per leg)	0.73	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10CTQ150SPbF 10CTQ150-1PbF	UNITS	
Maximum DC reverse voltage	V_{R}	150 V		
Maximum working peak reverse voltage	V_{RWM}	130	V	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER SYMBOL TEST CONDITIONS		ITIONS	VALUES	UNITS			
Maximum average forward current	per leg	1	50 % data and at T = 155 % and and a superferen		5	5	А
See fig. 5	per device	I _{F(AV)} 50 % duty cycle at T _C = 155 °C, rectangular waveform		, rectangular wavelonn	10	A .	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	620	А	
			10 ms sine or 6 ms rect. pulse		115		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH		5	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α	

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	5 A	T _J = 25 °C	0.93	V
Maximum forward voltage drop per leg		10 A		1.10	
See fig. 1		5 A	T _J = 125 °C	0.73	
		10 A		0.86	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.05	mΛ
See fig. 2		T _J = 125 °C		7	- mA
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.468	V
Forward slope resistance	r _t			28	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		200	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC} DC operation		3.50	
Maximum thermal resistance, junction to case per package				1.75	°C/W
Typical thermal resistance, case to heatsink (only for TO-220)		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf \cdot in)
Madinadada		Case style D ² PAK	10CTC	150S	
Marking device			Case style TO-262	10CTQ	150-1

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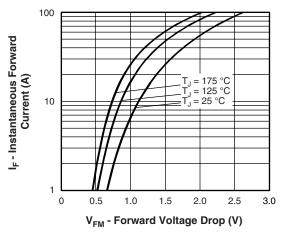


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

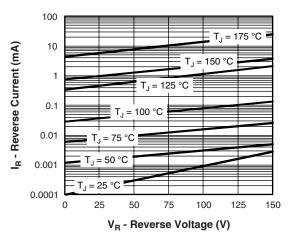


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

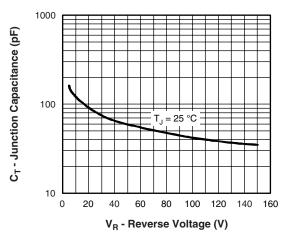


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

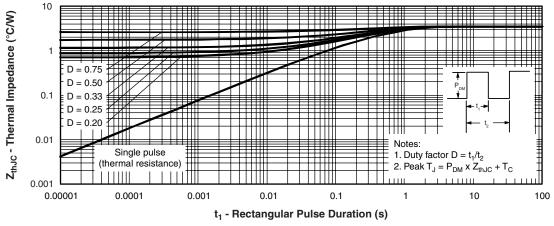


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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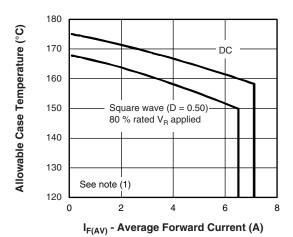


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

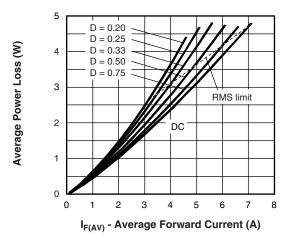


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

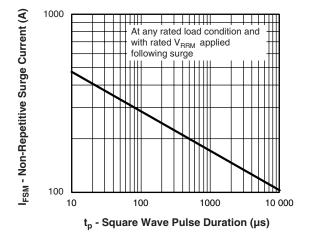


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

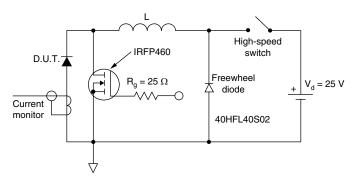


Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_{R}$ (1 - D); I_{R} at V_{R1} = 10 V

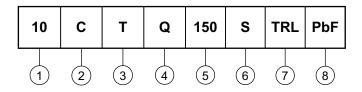


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ORDERING INFORMATION TABLE

Device code



1 - Current rating (10 A)

2 - Circuit configuration

C = Common cathode

3 - T = TO-220

- Schottky "Q" series

5 - Voltage rating (150 = 150 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			

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