VS-GB50LP120N



Vishay Semiconductors

Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 50 A

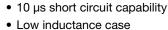


INT-A-PAK

PRODUCT SUMMARY				
V _{CES}	1200 V			
I _C at T _C = 80 °C	50 A			
$V_{CE(on)}$ (typical) at I _C = 50 A, 25 °C	1.7 V			
Speed	8 kHz to 30 kHz			
Package	INT-A-PAK			
Circuit	Chopper low side switch			

FEATURES

• High short circuit capability, self limiting to 6 x l_C



- · Fast and soft reverse recovery antiparallel FWD
- · Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- V_{CE(on)} with positive temperature coefficient
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultralow conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	v	
Collector current	L.	T _C = 25 °C	100		
	I _C	T _C = 80 °C	50		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	100	A	
Diode continuous forward current	١ _F		50		
Diode maximum forward current	I _{FM}		100		
Maximum power dissipation	PD	T _J = 150 °C	446	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
l ² t-value, diode	l ² t	V_{R} = 0 V, t = 10 ms, T _J = 125 °C	420	A ² s	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS ($T_C = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter saturation voltage	V _{CE(on)}	V_{GE} = 15 V, I_C = 50 A, T_J = 25 °C	-	1.70	-	v
		V_{GE} = 15 V, I _C = 50 A, T _J = 125 °C	-	1.95	-	
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 2 mA, T_J = 25 °C	5.0	6.2	7.0	
Zero gate voltage collector current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	1.0	mA
Gate to emitter leakage current	I _{GES}	V_{GE} = V_{GES} , V_{CE} = 0 V, T_{J} = 25 °C	-	-	400	nA

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RoHS COMPLIANT



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SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	220	-	ns mJ
Rise time	t _r		-	60	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 50 \text{ A}, \text{ R}_{g} = 18 \Omega,$	-	420	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	60	-	
Turn-on switching loss	E _{on}		-	2.1	-	
Turn-off switching loss	E _{off}		-	2.6	-	
Turn-on delay time	t _{d(on)}		-	270	-	ns
Rise time	t _r		-	60	-	
Turn-off delay time	t _{d(off)}	V_{CC} = 600 V, I _C = 50 A, R _g = 18 Ω, V _{GE} = ± 15 V, T _J = 125 °C	-	500	-	
Fall time	t _f		-	65	-	
Turn-on switching loss	E _{on}		-	4.1	-	
Turn-off switching loss	E _{off}		-	4.7	-	mJ
Input capacitance	C _{ies}		-	4.29	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	0.30	-	nF
Reverse transfer capacitance	C _{res}		-	0.20	-	
SC data	I _{SC}	$\label{eq:tsc} \begin{array}{l} t_{sc} \leq 10 \ \mu s, \ V_{GE} = 15 \ V, \ T_J = 125 \ ^{\circ}C, \\ V_{CC} = 900 \ V, \ V_{CEM} \leq 1200 \ V \end{array}$	-	270	-	А
Internal gate resistance	R _{gint}		-	10	-	Ω
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.75	-	mΩ

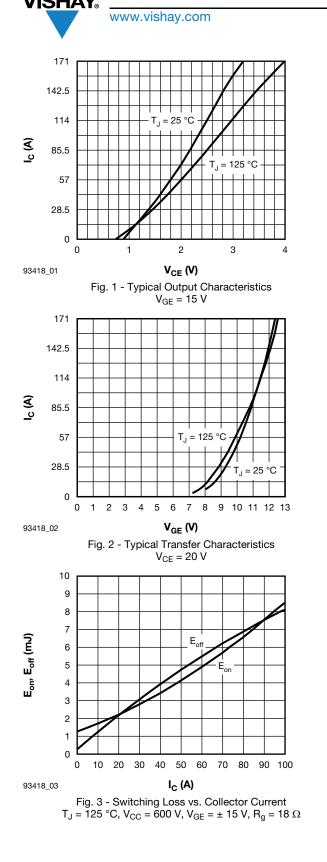
DIODE ELECTRICAL SPECIFICATIONS ($T_C = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage V _F	L 50 A	T _J = 25 °C	-	2.15	-	v	
	۷F	I _F = 50 A	T _J = 125 °C	-	2.35	-	7 V
Diode reverse recovery time t _{rr}	+	I _F = 50 A, V _R = 600 V, dl/dt = -2100 A/μs,	T _J = 25 °C	-	90	-	ns
	۲r		T _J = 125 °C	-	130	-	
Diode peak reverse recovery current			T _J = 25 °C	-	52	-	А
Diode peak reverse recovery current I _{RM}	$V_{GE} = -15 V$	T _J = 125 °C	-	60	-	~	
Diada rayaraa raaayary anarry	Е	62	T _J = 25 °C	-	1.9	-	mJ
Diode reverse recovery energy	E _{rec}		T _J = 125 °C	-	4.0	-	IIIJ

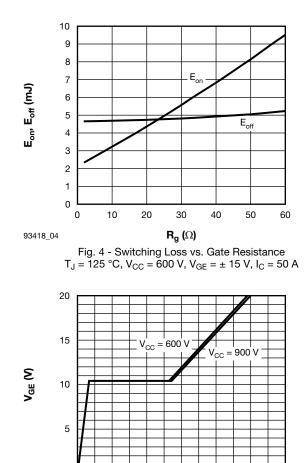
THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-40	-	150	°C
Storage temperature range	T _{Stg}		-40	-	125	
Junction to case IGBT	Б		-	-	0.28	
per ½ module Diode	R _{thJC}		-	-	0.65	K/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-	
Mounting torque		Power terminal screw: M5	2.5 to 5.0		Nm	
Mounting torque		Mounting screw: M6	3.0 to 6.0			
Weight				150		g

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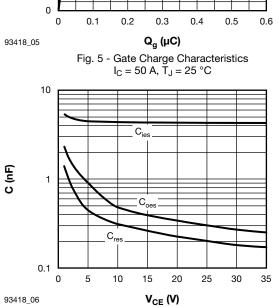


Fig. 6 - Typical Capacitance vs. Collector to Emitter Voltage

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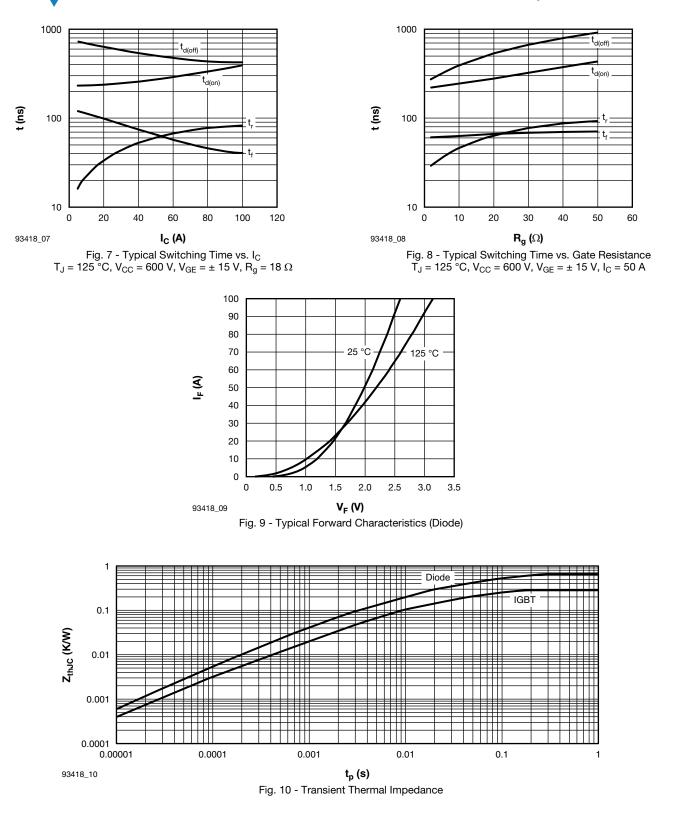
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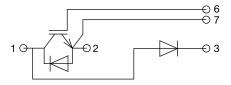
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CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			

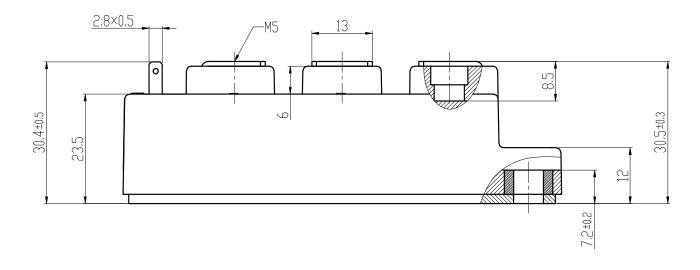
Outline Dimensions

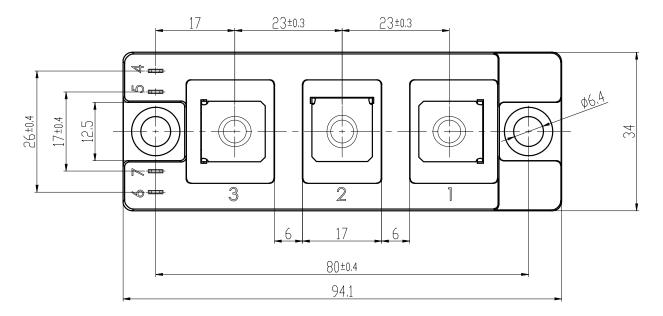


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INT-A-PAK

DIMENSIONS in millimeters (inches)







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