

Power Rectifier Diodes (T-Modules), 2200 V, 20 A



D-55

PRODUCT SUMMARY				
I _{F(AV)}	20 A			
Туре	Modules - Diode, High Voltage			
V _{RRM}	2200 V			
Package	D-55			
Circuit	Single diodes			

FEATURES

- Electrically isolated base plate
- 2200 V_{RRM}
- Industrial standard packaging
- UL approved file E78996



- · Simplified mechanical designs, rapid assembly
- Large creepage distances
- Designed and qualified for industrial level
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

These series of T-modules use standard recovery power rectifier diodes. The semiconductors are electrically isolated from the metal base, allowing common heatsink and compact assembly to be built.

Applications include power supplies, battery charges, welders, motor controls, and solar panel application.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	T20HF	UNITS		
		20	А		
I _{F(AV)}	T _C	85	°C		
I _{F(RMS)}		31			
I _{FSM}	50 Hz	450	Α		
	60 Hz	470			
l ² t	50 Hz	1015	A ² s		
	60 Hz	920	A-S		
I ² √t		10 125	A ² √s		
V _{RRM}		2200	V		
TJ		- 40 to 150	°C		

ELECTRICAL SPECIFICATIONS

VOLTAGE R	ATINGS			
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA
T20HF220	22	2200	2250	18



FORWARD CONDUCTION						
PARAMETER	SYMBOL		TEST CONDITION	T20HF	UNITS	
Maximum average forward current	I _{F(AV)}	180° conduct	ion, half sine wave	20	А	
at case temperature	-1 (AV)				85	°C
Maximum RMS forward current	I _{F(RMS)}				31	Α
Marian and and and		t = 10 ms	No voltage		450	
Maximum peak, one-cycle forward, non-repetitive		t = 8.3 ms	reapplied		470	Α
surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}	Sinusoidal	380	7
Sarge carrent		t = 8.3 ms	reapplied	half wave,	400	
		t = 10 ms	No voltage	initial T _J =	1015	
Manipular na 12t fau finaire a	l ² t	t = 8.3 ms	reapplied	T _J maximum	920	A ² s
Maximum I ² t for fusing		t = 10 ms	100 % V _{RRM}		715	
		t = 8.3 ms	reapplied		650	
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to	t = 0.1 ms to 10 ms, no voltage reapplied		10 125	A²√s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x T _J maximum	$I_{F(AV)} < I < \pi \times I_{F(AV)}$,	0.77	V
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}),$	T _J maximum	0.89	V	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J maximum			8.5	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)})$, T_J maximum			6.7	11152
Maximum forward voltage drop	V _{FM}	$I_{FM} = 60 \text{ A}, T_J = 25 \text{ °C},$ $t_p = 400 \text{ µs square pulse}$ $Average \text{ power} = V_{F(TO)} \times I_{F(AV)} + r_f \times (I_{F(RMS)})^2$			1.50	V

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	T20HF	UNITS
Maximum peak reverse leakage current	I _{RRM}	T _J = 150 °C	18	mA
RMS isolation voltage	V _{ISOL}	50 Hz, circuit to base, all terminals shorted $T_J = 25$ °C, t = 1 s	3500	V

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST COMPLETIONS		VALUES VALUES		VALUES	UNITS
PANAMETER	SYMBOL TEST CONDITIONS		T20HF	UNITS			
Maximum junction operating and storage temperature range	T _J , T _{Stg}			- 40 to 150	°C		
Maximum thermal resistance, junction to case per junction	R _{thJC}	DC operation		2.53	K/W		
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface smooth, flat and greased		0.2	- K/VV		
Mounting torque, to heatsink		Non-lubricated	M3.5 mounting screws (1)	1.3 ± 10 %	Nm		
± 10 % terminals		threads	M5 screw terminals	3 ± 10 %	INITI		
Approximate weight		See dimensions - link at the end of datasheet		54	g		
Case style				T-module	(D-55)		

Note

⁽¹⁾ A mounting compound is recommended and the torque should be rechecked after a period of about 3 hours to allow for the spread of the compound

∆R CONDUC	CTION P	ER JUN	CTION								
DEVICES	SINUSC	SINUSOIDAL CONDUCTION AT TJ MAXIMUM					RECTANGULAR CONDUCTION AT T _J MAXIMUM				UNITS
DEVICES	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
T20HF	0.29	0.34	0.43	0.64	1.10	0.20	0.35	0.47	0.67	1.11	K/W

Note

Revision: 07-Jun-13 2 Document Number: 94709

[•] Table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC



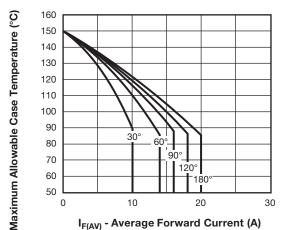


Fig. 1 - Current Ratings Characteristics

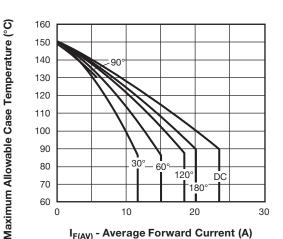


Fig. 2 - Current Ratings Characteristics

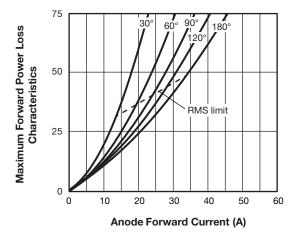


Fig. 3 - Forward Power Loss Characteristics

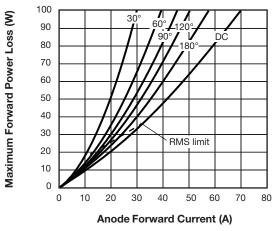


Fig. 4 - Forward Power Loss Characteristics

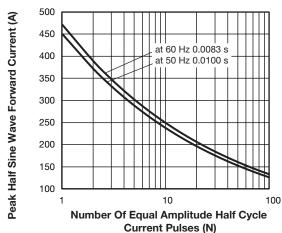


Fig. 5 - Maximum Non-Repetitive Surge Current

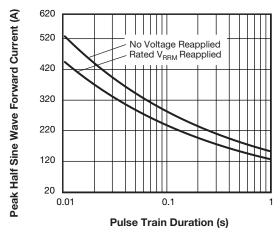


Fig. 6 - Maximum Non-Repetitive Surge Current



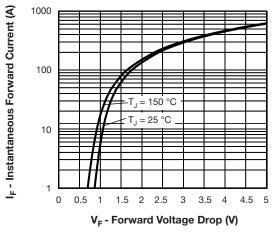


Fig. 7 - Forward Voltage Drop Characteristics

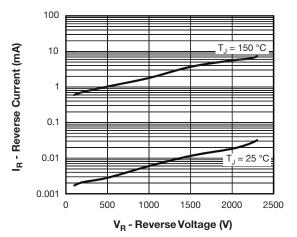


Fig. 8 - Typical Values of Reverse Current vs. Reverse Voltage

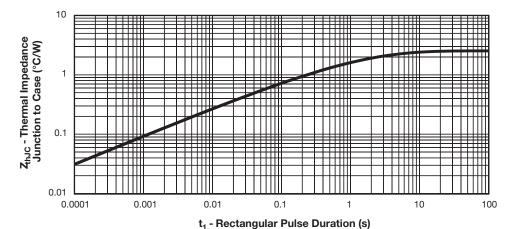
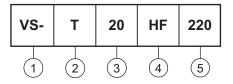


Fig. 9 - Maximum Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Module type

3 - Current rating

4 - Circuit configuration (see Circuit Configuration table)

Voltage code x 10 = V_{RRM}

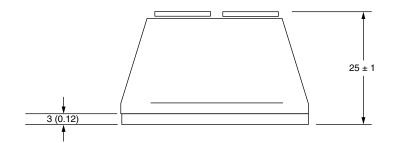
CIRCUIT CONFIGURATION					
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Single diode	HF	+ 0			

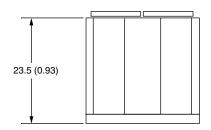
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95313

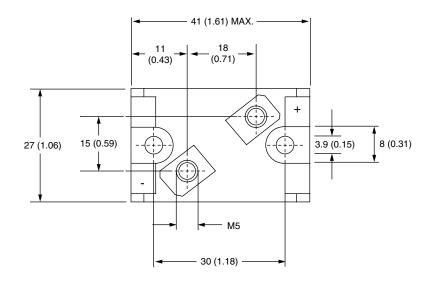


D-55 T-Module Diode Standard and Fast Recovery

DIMENSIONS in millimeters (inches)









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